

# DAY #1 – May 15, 2017

Time	Description
8:00-9:00	<b>Registration and Breakfast</b> Sign in, make yourself a cup of coffee, and meet your fellow engineers.
9:00-10:30	<b>Power Integrity Ecosystem</b> Heidi Barnes EEs of EDA High Speed Digital Specialist, Keysight
10:30-10:45	Break
10:45-12:15	<b>Optical signal characterization in PAM4 links</b> Pavel Zivny, Tektronix
12:15-13:15	Lunch
13:15-14:45	To be published
14:45-15:00	Break
15:00-16:00	<b>Enabling 56 Gbps Solutions in Printed Circuit Board Technology</b> Christine Marvell- Harrington, Amphenol

# DAY #2 – May 16,2017

Time	Description
8:00-9:00	<a href="#">Registration and Breakfast</a> Sign in, make yourself a cup of coffee, and meet your fellow engineers.
9:00-10:30	<a href="#">Part #1: Power integrity for printed circuit board design</a> James L. Drewniak PhD
10:30-10:45	Break
10:45-12:15	<a href="#">Part #2: Power integrity for printed circuit board design</a> James L. Drewniak PhD
12:15-13:15	Lunch
13:15-14:45	To be published
14:45-15:00	Break
15:00-16:00	To be published

# DAY #3 – May 17, 2017

Time	Description
8:00-9:00	<b>Registration and Breakfast</b> Sign in, make yourself a cup of coffee, and meet your fellow engineers.
9:00-10:30	<b>Part #1: PCB Trace and Via Temperatures; How To Deal With Real World Variables</b> Douglas G. Brooks, PhD President, UltraCAD Design, Inc.
10:30-10:45	Break
10:45-12:15	<b>Part #2: PCB Trace and Via Temperatures; How To Deal With Real World Variables</b> Douglas G. Brooks, PhD President, UltraCAD Design, Inc.
12:15-13:15	Lunch
13:15-14:45	<b>First session: Radiated Emissions: Design Basics, Troubleshooting, and Precompliance Testing</b> Kenneth Wyatt, President and Principle Consultant, Wyatt Technical Services LLC
14:45-15:00	Break
15:00-16:00	<b>Second Session: Radiated Emissions: Design Basics, Troubleshooting, and Precompliance Testing</b> Kenneth Wyatt, President and Principle Consultant, Wyatt Technical Services LLC