

AGENDA

dgcon 2014

Dan Accadia, Herzliya

Time Description *Day one* **24.11.14**

08:00 - 09:00	Registration & Gathering
09:00 - 09:45	Opening Lecture - Mr. DUDI TASH, SI/PI Expert, Dgtronix
09:45 - 10:45	Reducing losses in differential channels (Part A) - Dr. ERIC BOGATIN, Signal Integrity Evangelist, Teledyne LeCroy
10:45 - 11:00	Coffee Break
11:00 - 12:00	Reducing losses in differential channels (Part B) - Dr. ERIC BOGATIN, Signal Integrity Evangelist, Teledyne LeCroy
12:00 - 13:00	Lunch
13:00 - 13:45	Reducing losses in differential channels (Part C) - Dr. ERIC BOGATIN, Signal Integrity Evangelist, Teledyne LeCroy
13:45 - 14:00	Coffee Break
14:00 - 15:30	Power distribution measurements: challenges and solutions - Live Video HD - Dr. ISTVAN NOVAK, Senior Principal Engineer, Oracle
15:30 - 15:45	Coffee Break
15:45 - 16:45	Interconnect in 25Gbit Signal Integrity guidelines - Mr. BEN RUBOVITCH, AITC Senior Signal Integrity Development Engineer, Amphenol TCS
16:45 - 17:15	Panel - Ask the Experts, Anything Goes - Dr. ERIC BOGATIN, Signal Integrity Evangelist, Teledyne LeCroy - Mr. BEN RUBOVITCH, AITC Senior Signal Integrity Development Engineer, Amphenol TCS - Mr. DUDI TASH, SI/PI Expert, Dgtronix

Time Description *Day two* **25.11.14**

08:00 - 09:00	Registration & Gathering
09:00 - 09:15	Opening Speech - Mr. DUDI TASH, SI/PI Expert, Dgtronix
09:15 - 10:45	The Role of Vias in Highspeed Design - Dr. AVRI FRENKEL, Representative and Tech Support of CST in Israel, Anafa Electro magnetic Solutions Ltd.
10:45 - 11:00	Coffee Break
11:00 - 12:00	TBD - Mr. HAIM SPIEGEL, EDA RF Specialist, Agilent Technologies
12:00 - 13:00	Lunch
13:00 - 14:00	Package and PCB Simulations for EMI compatibility - Mr. SHAI SAYFAN ALTMAN, Application Engineer, ANSYS
14:00 - 14:45	DDR4 and LPDDR4 Board Design and Verification Challenges - Mr. NITIN BHAGWATH, Technical Marketing Engineer, Mentor Graphics
14:45 - 15:00	Coffee Break
15:00 - 16:00	POD DDR Interface Power Noise - Mr. ELDAD BAR-LEV, Staff Signal Integrity Engineer, Marvell Israel Ltd.
16:00 - 16:15	Coffee Break
16:15 - 17:00	Base Materials Development: Reliability in Low Loss/High Frequency applications - Mr. ALUN MORGAN, Director of OEM Marketing for Isola Group Europe and Managing Director of Isola UK
17:00 - 17:30	Truth is, the Eye is only in the beholder's mind - Mr. ADEE RAN, PHY architect, Communication & Storage Infrastructure Group, Intel Corporation
17:30 - 17:45	Closing Speech - Mr. DUDI TASH, SI/PI Expert, Dgtronix

*Conference Agenda is Subject to Changes

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